| Electronic Patent Application Fee Transmittal | | | | | | |
|---|------------------------------|----------|--------|-------------------------|--|--|
| Application Number: | 10701058 | | | | | |
| Filing Date: | 04-Nov-2003 | | | | | |
| Title of Invention: | Frequency regulating circuit | | | | | |
| First Named Inventor/Applicant Name: | Holger Sedlak | | | | | |
| Filer: | Laura C. Brutman/Kim Padilla | | | | | |
| Attorney Docket Number: | 10046.0155 | | | | | |
| Filed as Large Entity | | | | | | |
| Utility under 35 USC 111(a) Filing Fees | | | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) | | |
| Basic Filing: | | | | | | |
| Pages: | | | | | | |
| Claims: | | | | | | |
| Miscellaneous-Filing: | | | | 11. | | |
| Petition: | | | | | | |
| Patent-Appeals-and-Interference: | | | | | | |
| Post-Allowance-and-Post-Issuance: | | | | | | |
| Extension-of-Time: | | | | | | |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|-----------------------------------|-------------------|----------|--------|-------------------------|
| Miscellaneous: | | | | |
| Request for continued examination | 1801 | 1 | 810 | 810 |
| | Total in USD (\$) | | | 810 |